





U2:CPU	U1:MP3 DSP	R83	R84
RS9702-001 ->	S5L9274	OPEN	47K
RS9702-001 ->	S5L9276		
RS9702-01C ->	S5L9274	OPEN	OPEN
RS9702-01C ->	S5L9276	OPEN	SHORT

VCC  
VREF  
E  
B  
C  
D  
A  
F  
MD  
VR  
LD  
GND  
FO-  
TR-  
TR+  
FO+

SF-C20

(BROWN) FEED(+)  
(BLACK) FEED(-)

(BLACK) SPINDLE(-)  
(RED) SPINDLE(+)

(RED) SLED+  
(BLACK) SLED-  
(WHITE) SLORG\_IN  
(BLUE) SLORG\_OUT

(BLACK) GND  
(RED) EJ END  
(WHITE) LD START

J3  
CD DATA  
CD-1  
3VDC  
SENS IO  
D-GND  
S VDC  
CDP BUS  
CDP BUS  
ACC(CDP ON)  
SENS IN  
RESET  
BUP(SV)  
CDP R-CH  
A-GND  
CDP L-CH

DSP-MP3

CPU

DRIVER

ASP

CD-DSP

16M  
DRAM

EFM

RFO  
0.7V P-P -0.9V P-P

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