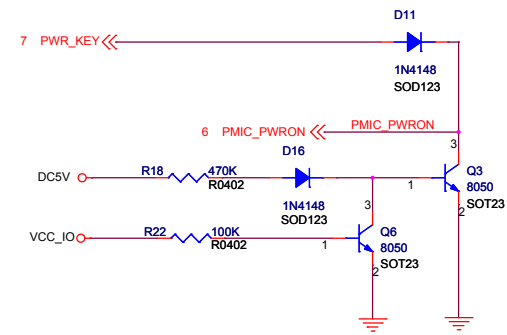


Table 12. Power Sources

RESOURCE	TYPE	VOLTAGES	POWER
VIO	SMPS	1.5 V / 1.8 V / 2.5 V / 3.3 V	1000 mA
VDD1	SMPS	0.6 ... 1.5 in 12.5-mV steps Programmable multiplication factor: x2, x3	1500 mA
VDD2	SMPS	0.6 ... 1.5 in 12.5-mV steps Programmable multiplication factor: x2, x3	1500 mA
VDD3	SMPS	5 V	100 mA
VDIG1	LDO	1.2 V, 1.5 V, 1.8 V, 2.7 V	300 mA
VDIG2	LDO	1 V, 1.1 V, 1.2 V, 1.8 V	300 mA
VPLL	LDO	1.0 V, 1.1 V, 1.8 V, 2.5 V	50 mA
VDAC	LDO	1.8 V, 2.6 V, 2.8 V, 2.85 V	150 mA
VAUX1	LDO	1.8 V, 2.5 V, 2.8 V, 2.85 V	300 mA
VAUX2	LDO	1.8 V, 2.8 V, 2.9 V, 3.3 V	150 mA
VAUX33	LDO	1.8 V, 2.0 V, 2.8 V, 3.3 V	150 mA
VMMC	LDO	1.8 V, 2.8 V, 3.0 V, 3.3 V	300 mA

POWER DIAGRAM

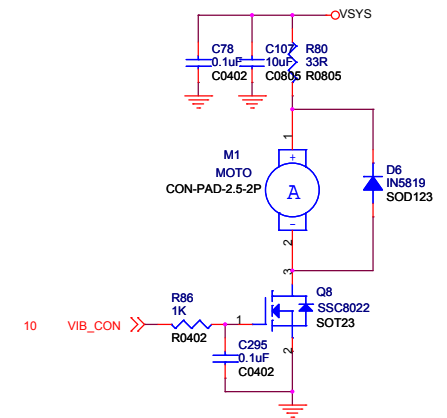
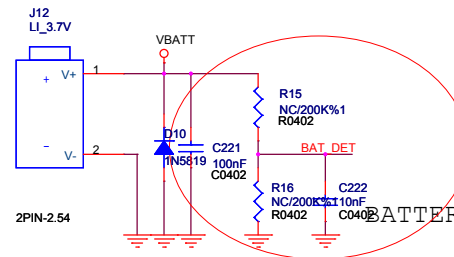
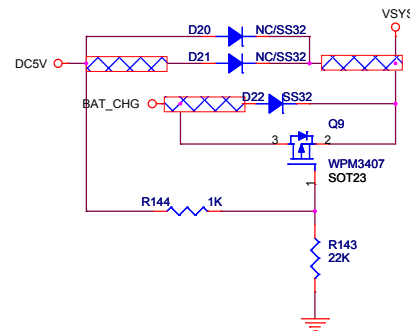
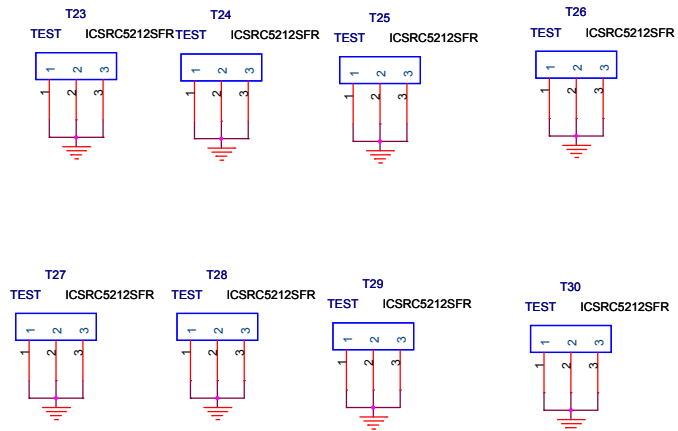


POWER CONTROL

BATTERY CHARGE

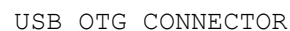
PCB POWER WIRE WIDTH INDICATE

- above 80 miles
- above 50 miles
- above 30 miles
- above 12 miles
- Under needs

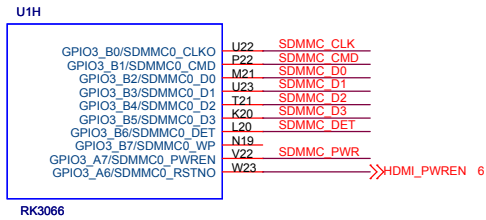


VIBRATION

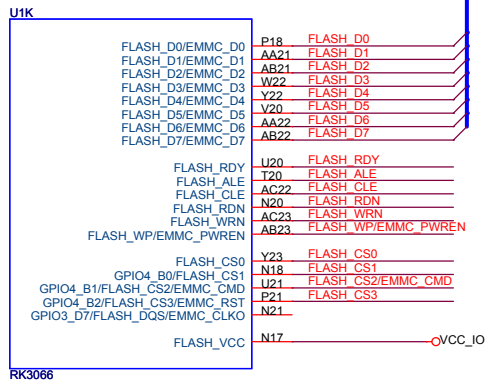






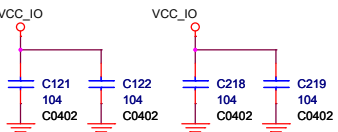
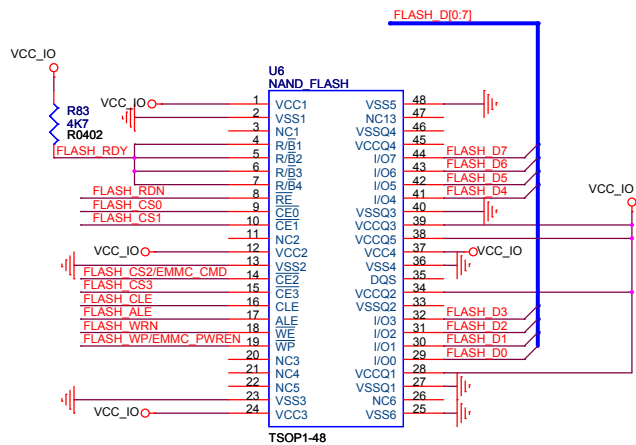


RK3066-H

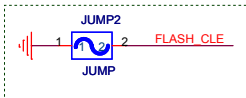


RK3066-K

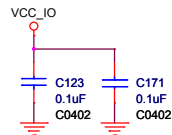
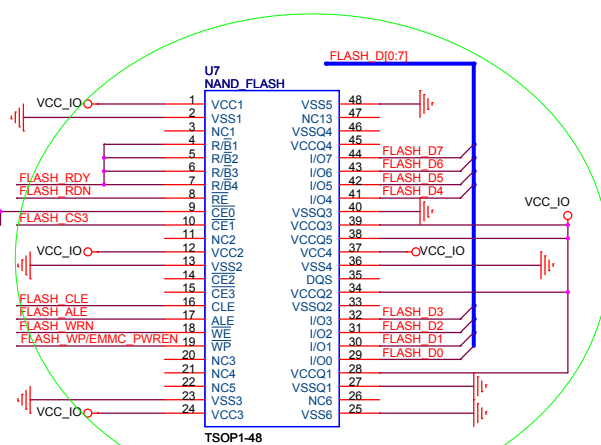
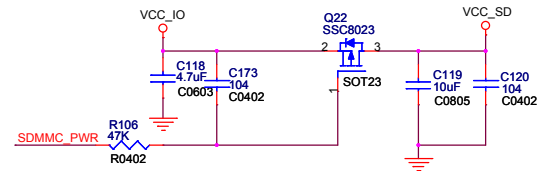
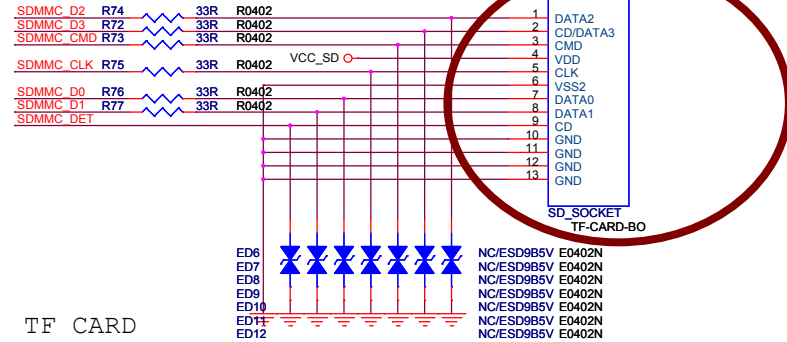
Note:  
Select the power type  
follow FLASH IO need.



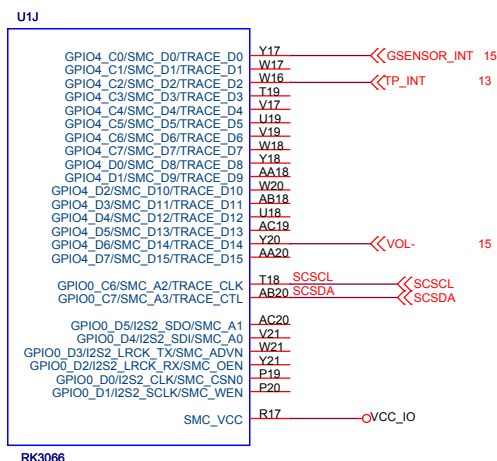
Note:  
Reserve a PAD.



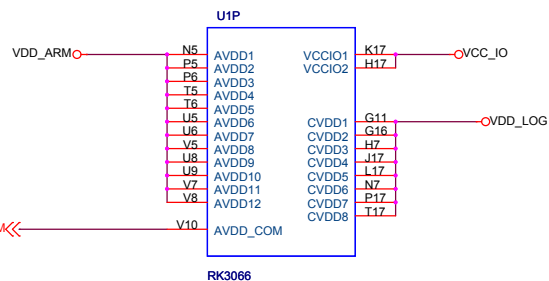
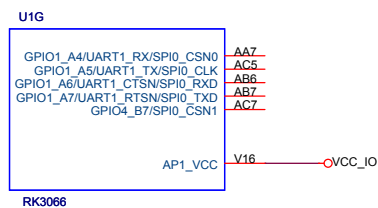
NAND FLASH (OPTION1)



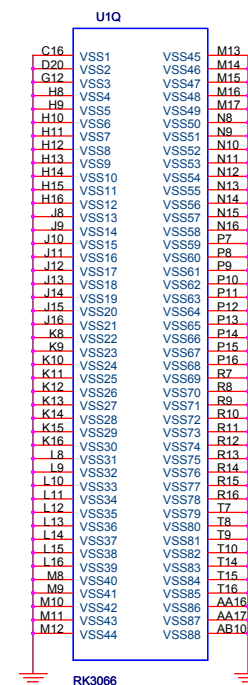
Schematic: 06.Flash/TF card			
Size	Project	Rev	
A3	TVE723S	1.0	
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RK3066-G

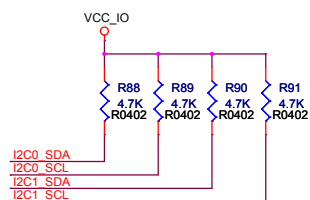
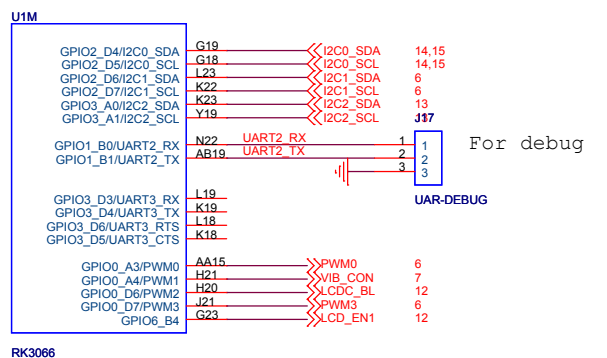


Note:  
Place these filter capacitors under CPU.

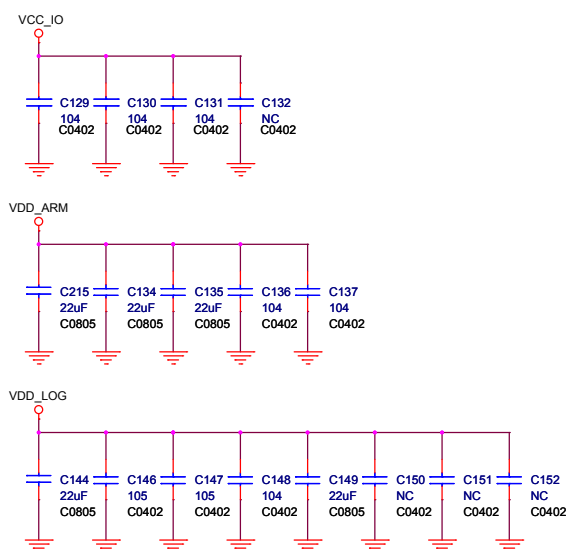


RK3066-Q

RK3066-J



RK3066-M



RK3066 CORE POWER FILTER

Schematic: 07.GPIO			
Size A3	Project TVE723S		Rev 1.0
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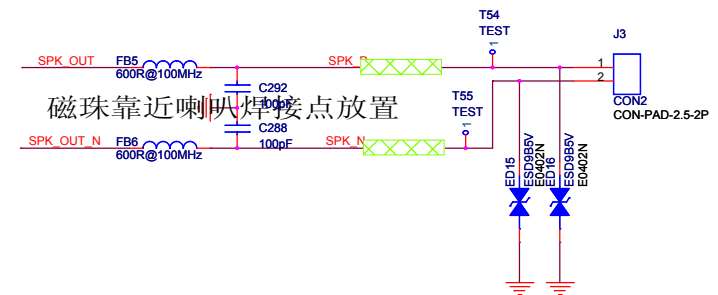
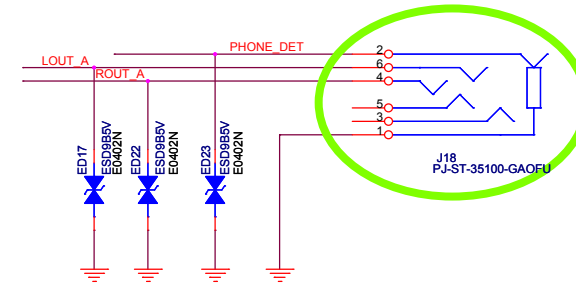
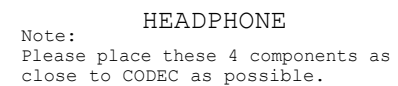
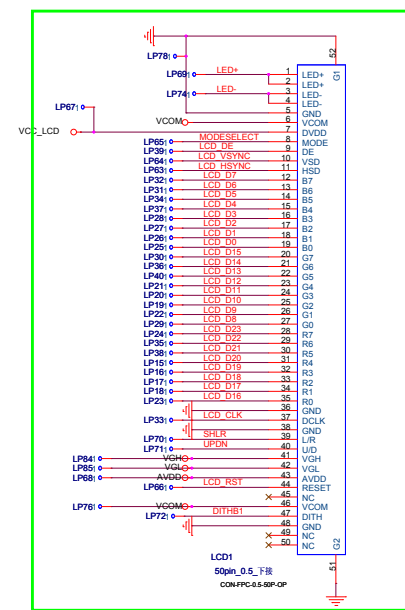
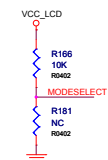
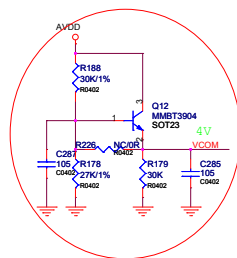
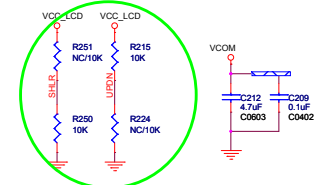
[illegible]

Figure 10: I2C0 pin connections. The diagram shows the I2C0 pin connections for the I2C0 module. It includes pins for I2S0 SD00, I2S0 SDI, I2S0 CLK, I2S0 SCLK, I2S0 LRCK TX, I2S0 LRCK RX, I2C0\_SCL, and I2C0\_SDA. The connections are as follows: I2S0 SD00 to R111, I2S0 SDI to R108, I2S0 CLK to R105, I2S0 SCLK to R104, I2S0 LRCK TX to R96, I2S0 LRCK RX to R95, I2C0\_SCL to I2C0\_SDA, and I2C0\_SDA to I2C0\_SDA. The I2C0 module is connected to the I2S0 module via the I2C0\_SCL and I2C0\_SDA pins.

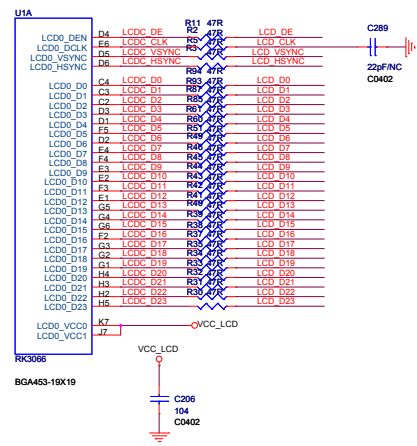
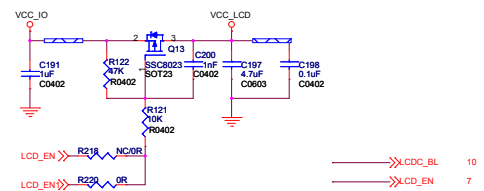
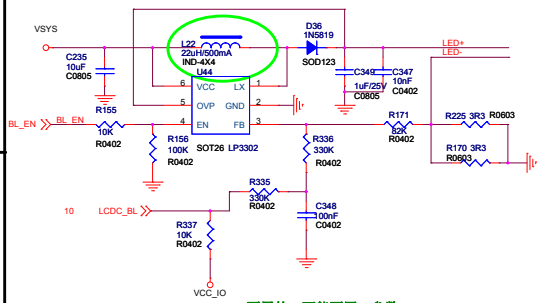
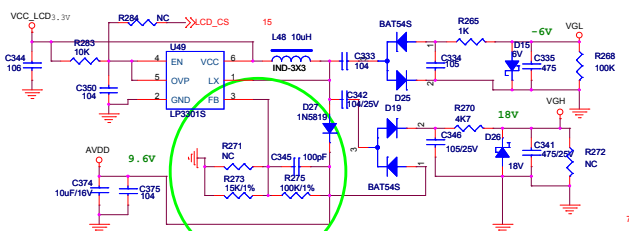
SPEAKER

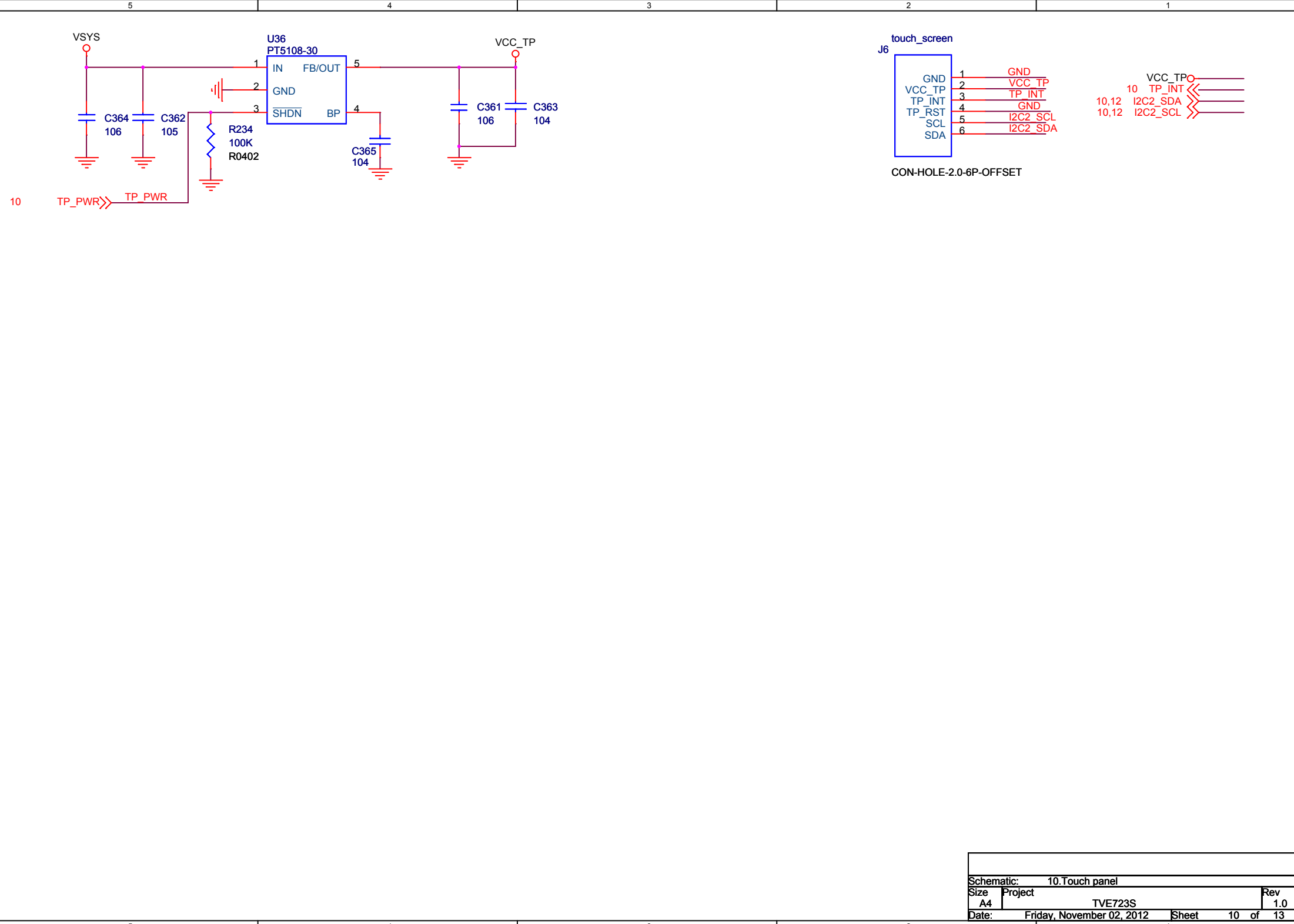
Schematic: 08.Audio				
Size	Project			Rev
A3	TVE723S			1.0
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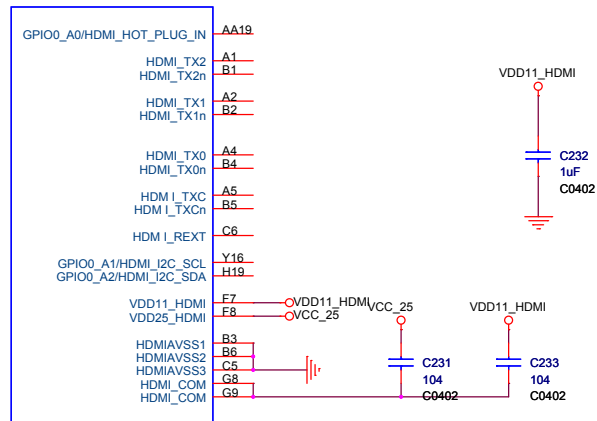


RGB PANEL CONNECTOR





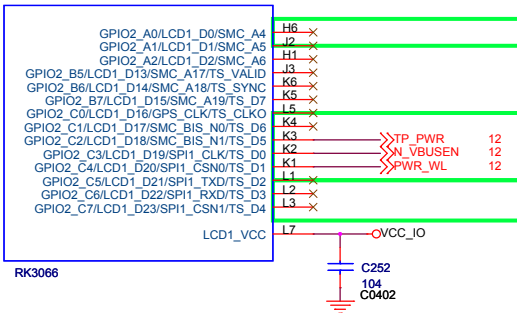
U10



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RK3066-O

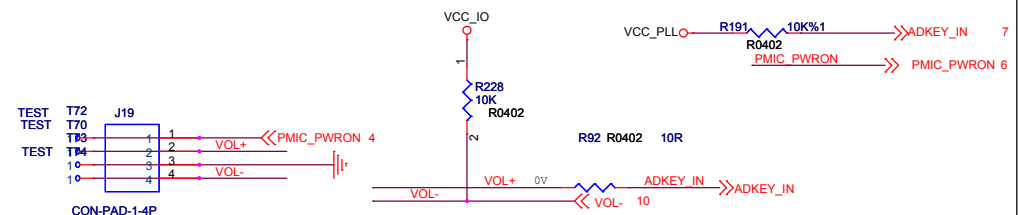
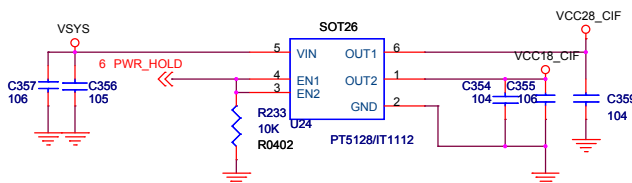
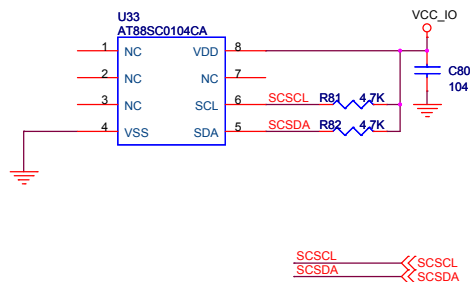
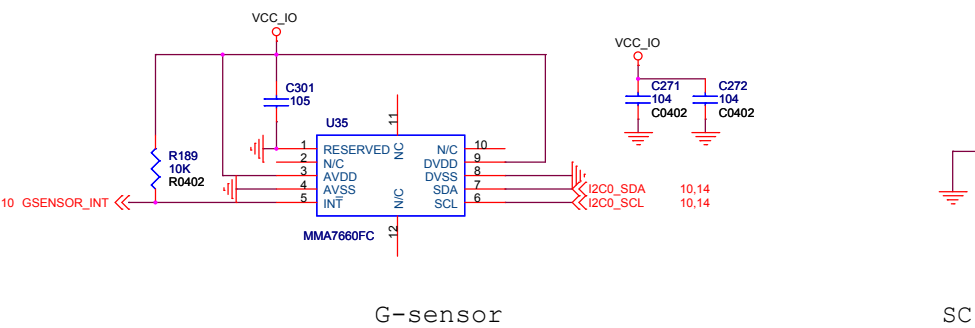
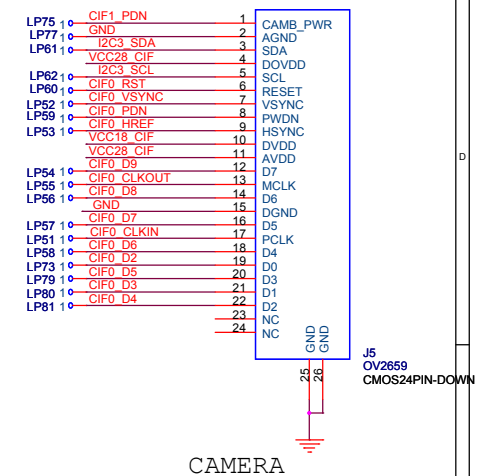
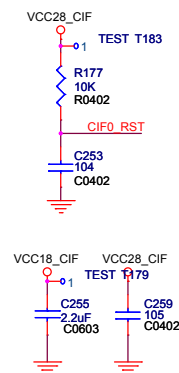
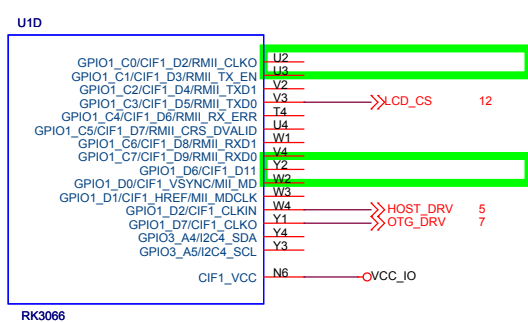
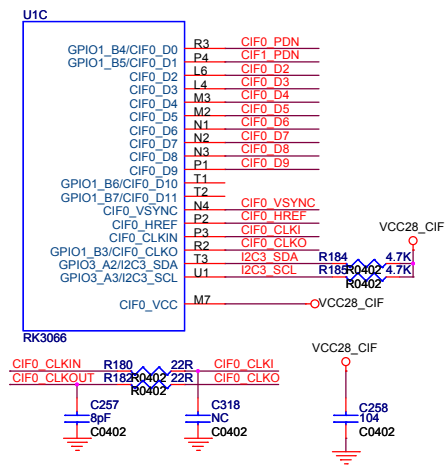
U1B



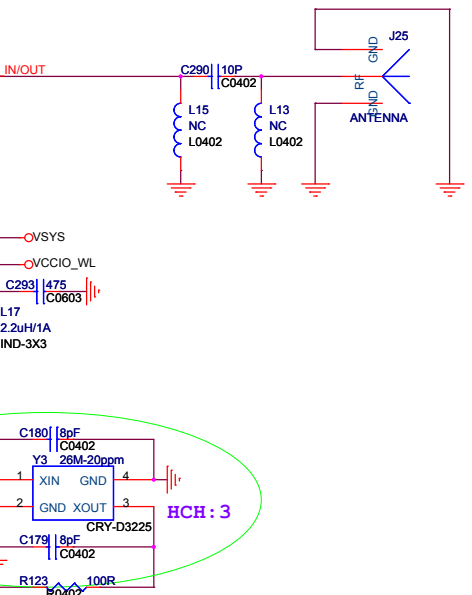
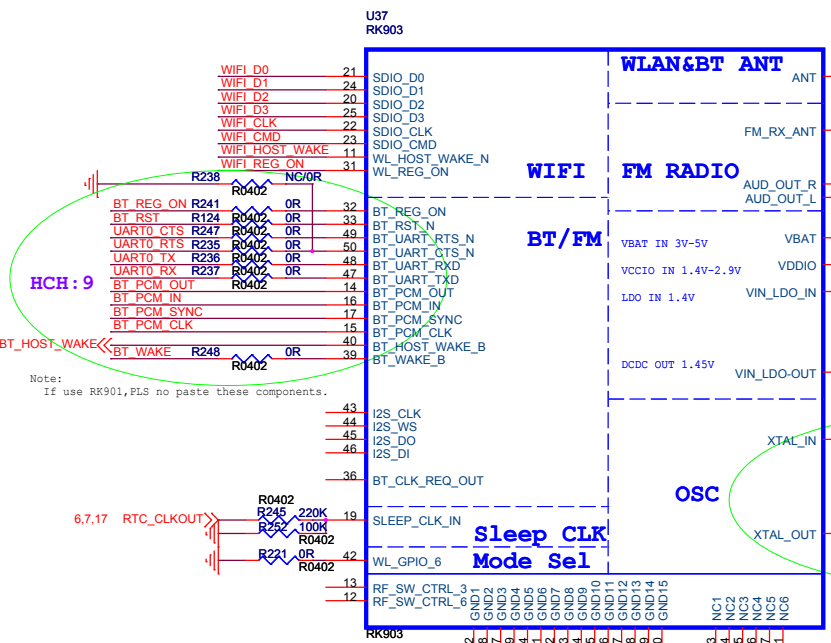
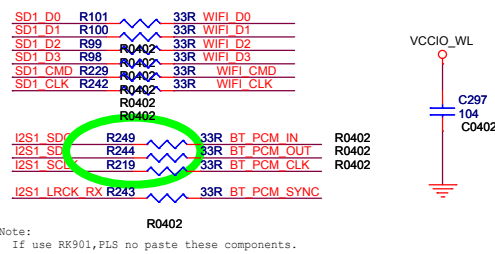
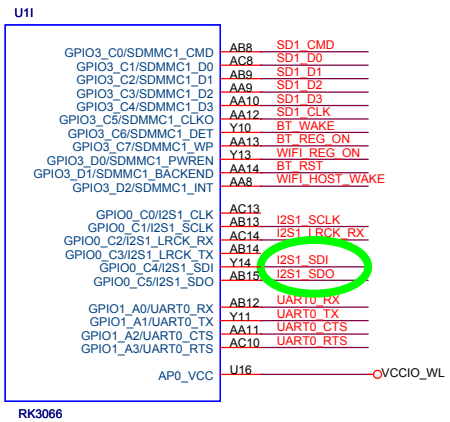
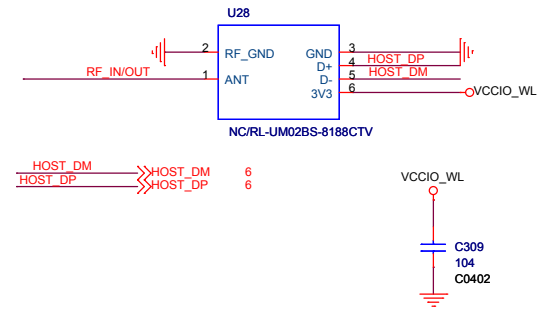
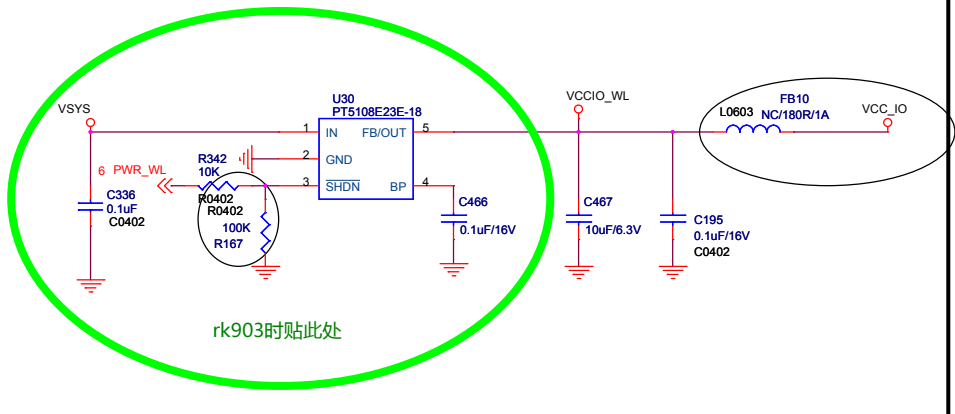
RK3066

RK3066-B

Schematic: 11.HDMI			
Size	Project	Rev	
A3	TVE723S	1.0	
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When the system power on, the Adkey in level is 0V, RK30 enter into recover mode.



Module	W_GPIO_6	BT_UART_CTS_N
RK903	Ground	Floating
RK901	Floating	Ground

RK3066-I

Note :PLS design the RF under RK RF LAYOUT guide;  
For more suggestions, please refer to the SPEC  
of the wireless IC

WIFI+BT+FM MODULE