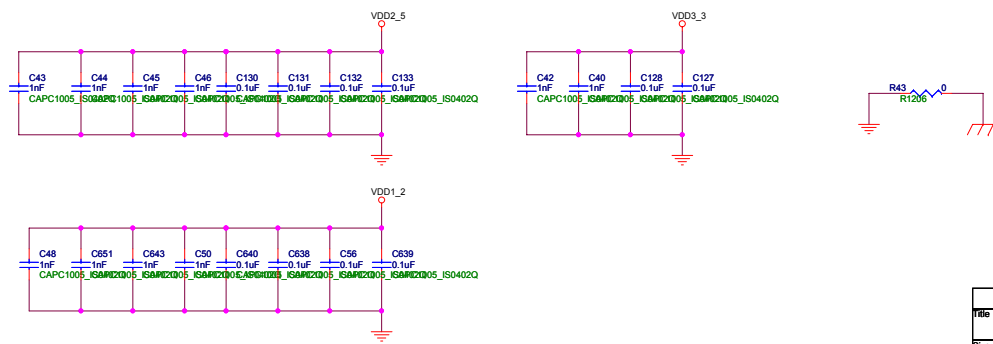
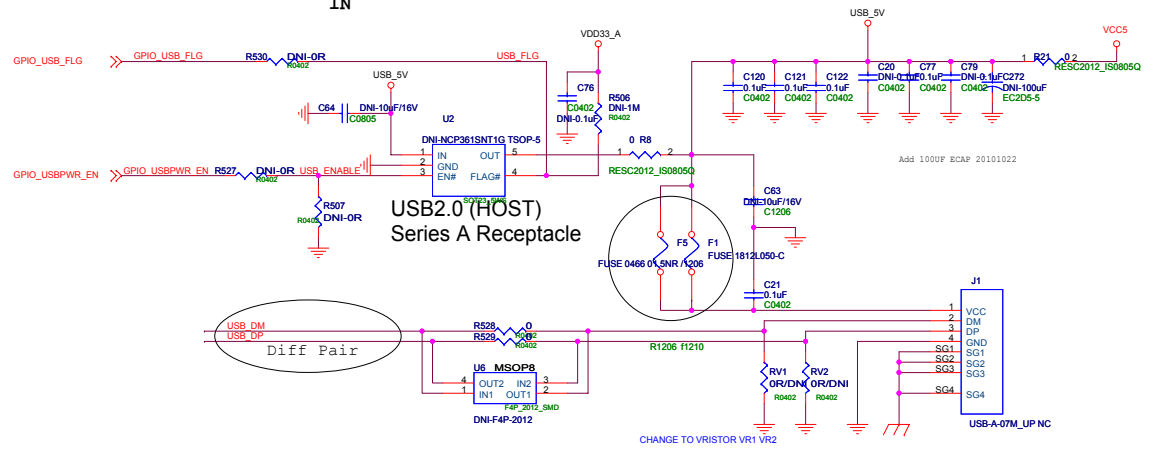
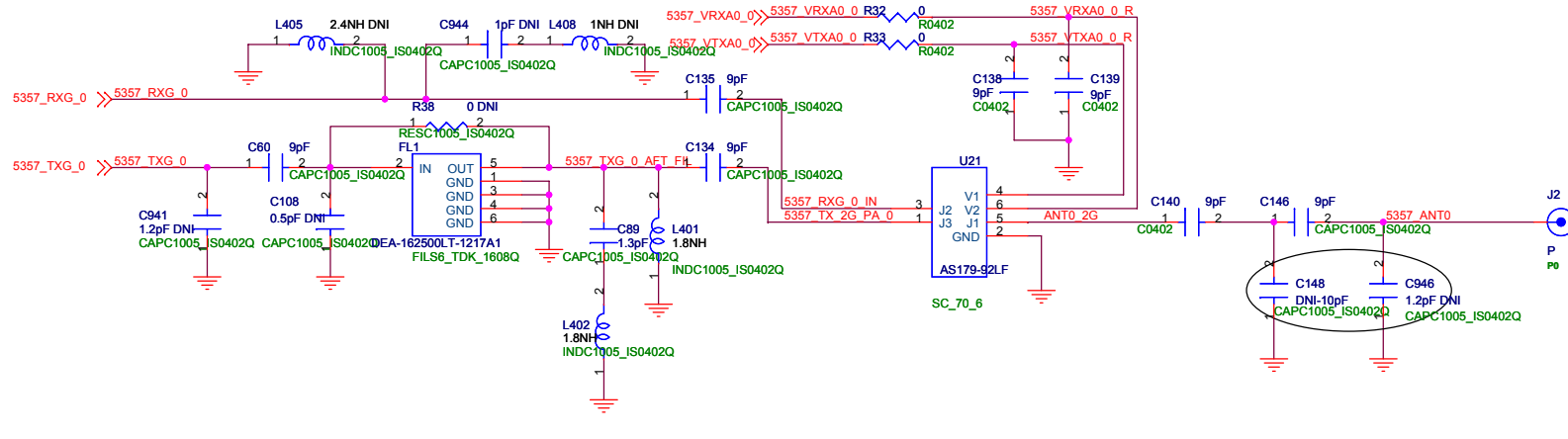


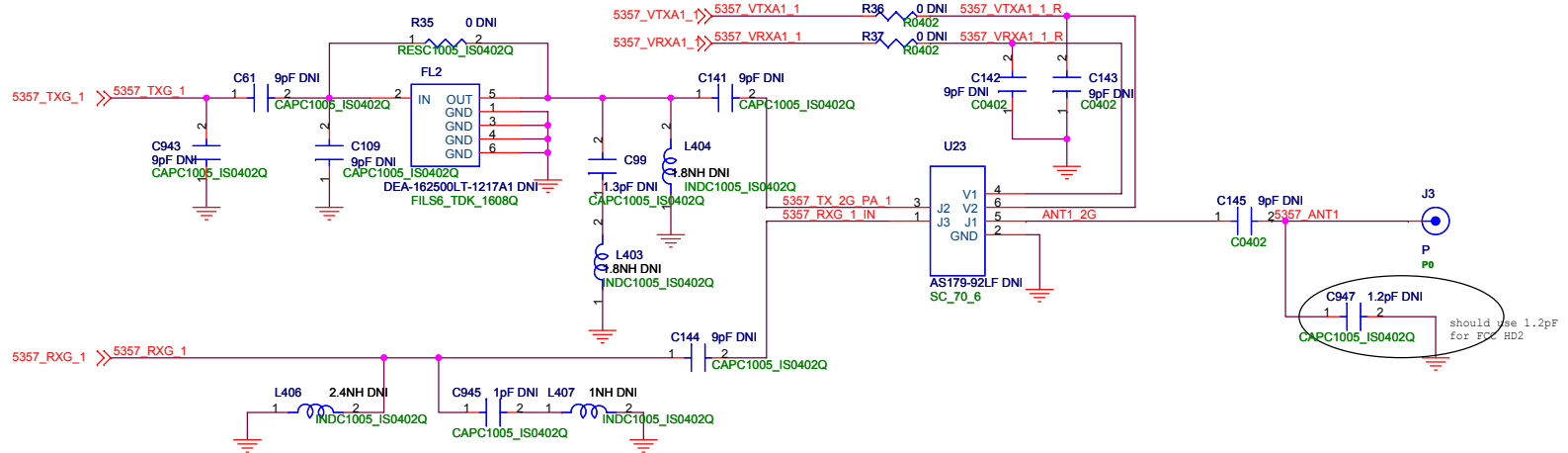
FOR USB APPLICATION,
NEED 5V REGULATED DC
IN



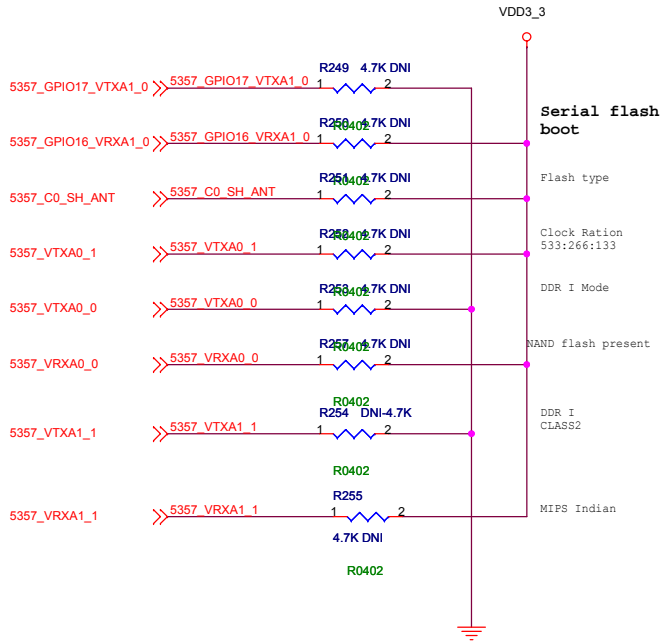
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POWER_GND_USB		
Size	Document Number	Rev
C	RT-N10U	A
Date:	Monday, May 23, 2011	Sheet 1 of 1



For USE ONLY

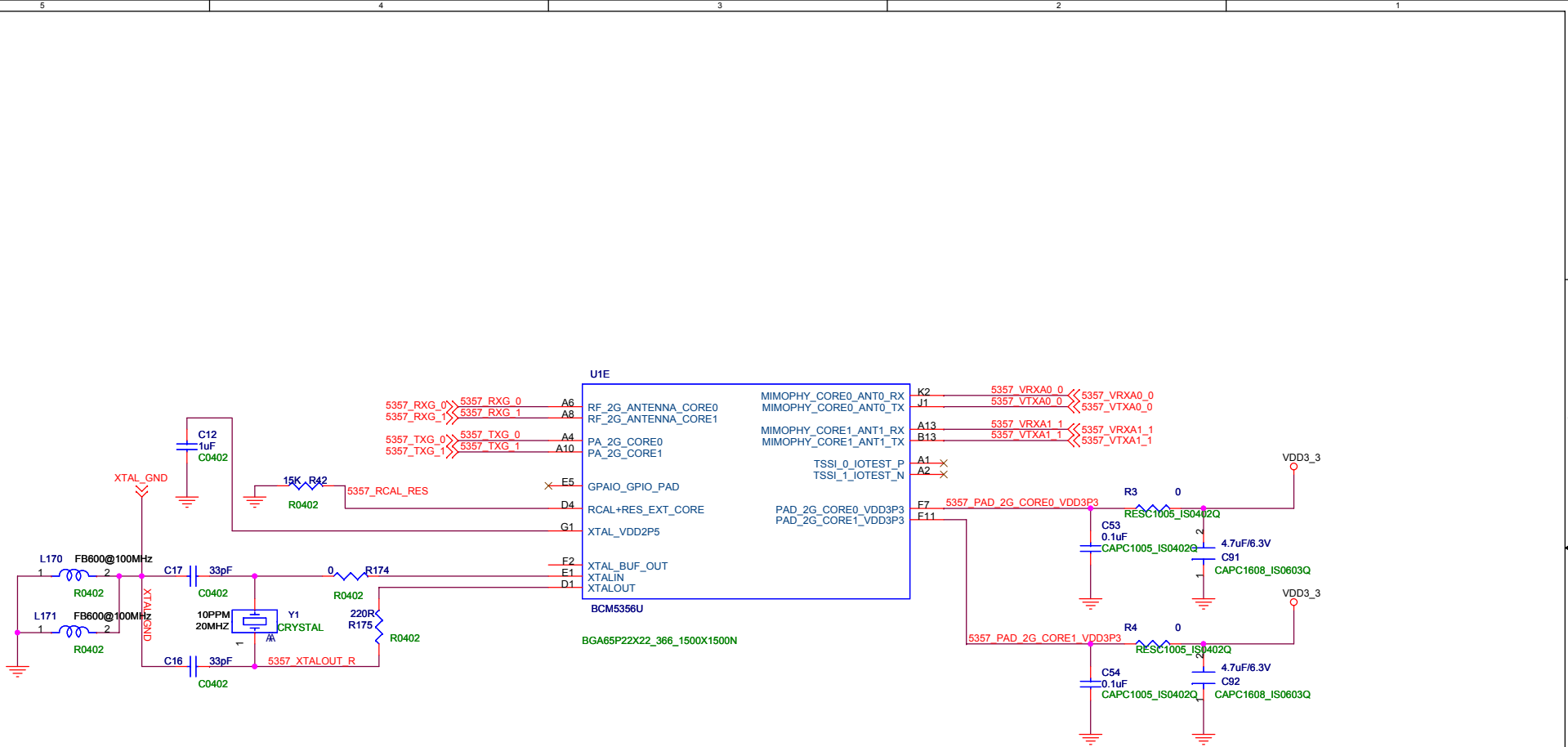


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Size	Document Number	Rev	
B	RT-N10U	A	
Date:	Monday, May 23, 2011	Sheet	2 of 8

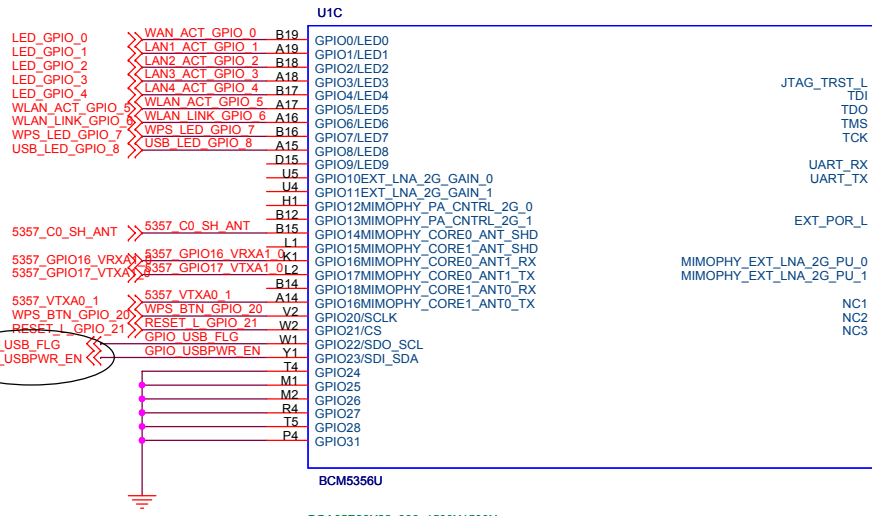


Pin name	Default	Setting	Description	Value=0	Value=1
5357_GPIO17_VTXA1_0 [GPIO17] 5357_GPIO16_VRXA1_0 [GPIO16]	10	10	Boot Type 00:DDR boot 01:ROM 10:Serial Flash boot 11:Nand boot		
5357_C0_SH_ANT GPIO14/mimophy_core0_ant_shd	0	0	Flash type	ST Serial Flash	Atmel Serial Flash
5357_VTXA0_1 GPIO19/mimophy_core1_ant0_tx	0	0	Clock Ratio	mips:PI301a:PI301b=133:133:133	mips:PI301a:PI301b=533:266:133
5357_VRXA0_0 mimophy_core0_ant0_rx	1	1	DDR I or II Mode	DDR2 Mode	DDR1 Mode
5357_VRXA0_0 mimophy_core0_ant0_rx	0	0	NAND Flash Present	NAND Flash not present	NAND Flash present
5357_VTXA1_1 mimophy_core1_ant1_tx	0	1	DDR_I Class	DDR1 sst1_class2	DDR1 sst1_class1
5357_VRXA1_1 mimophy_core1_ant1_rx	0	0	MIPS_Endian	Little Endian	Big Endian

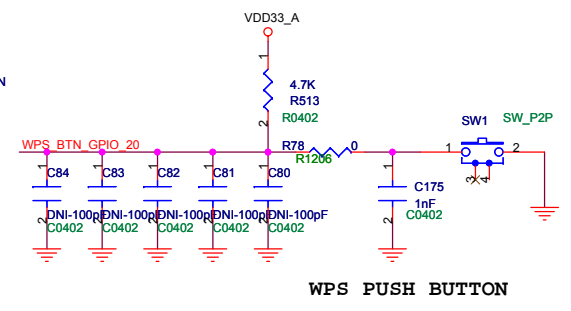
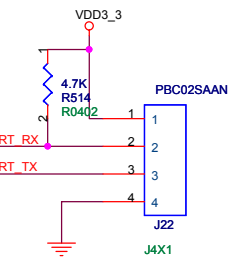
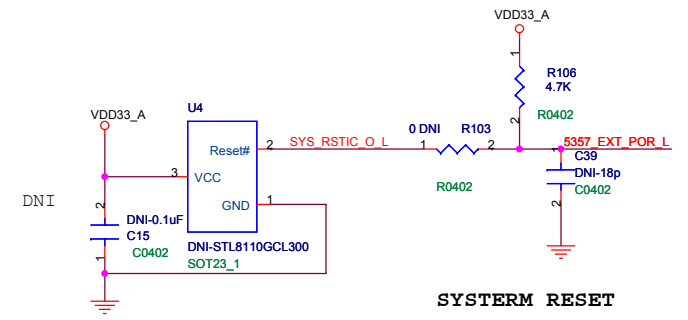
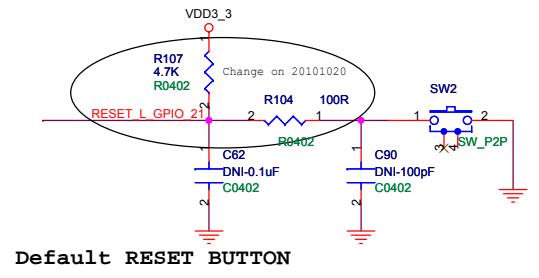
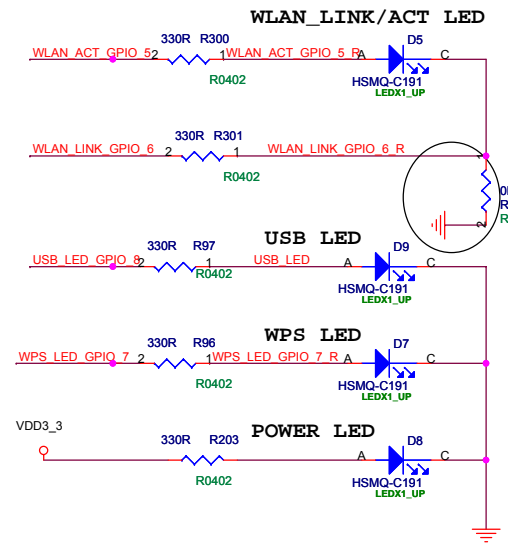
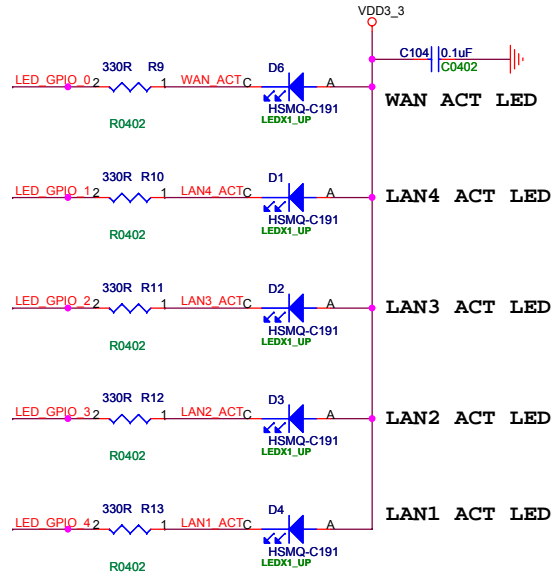
Title BOOT Strapping		
Size B	Document Number RT-N10U	Rev A
Date: Monday, May 23, 2011 Sheet 3 of 8		



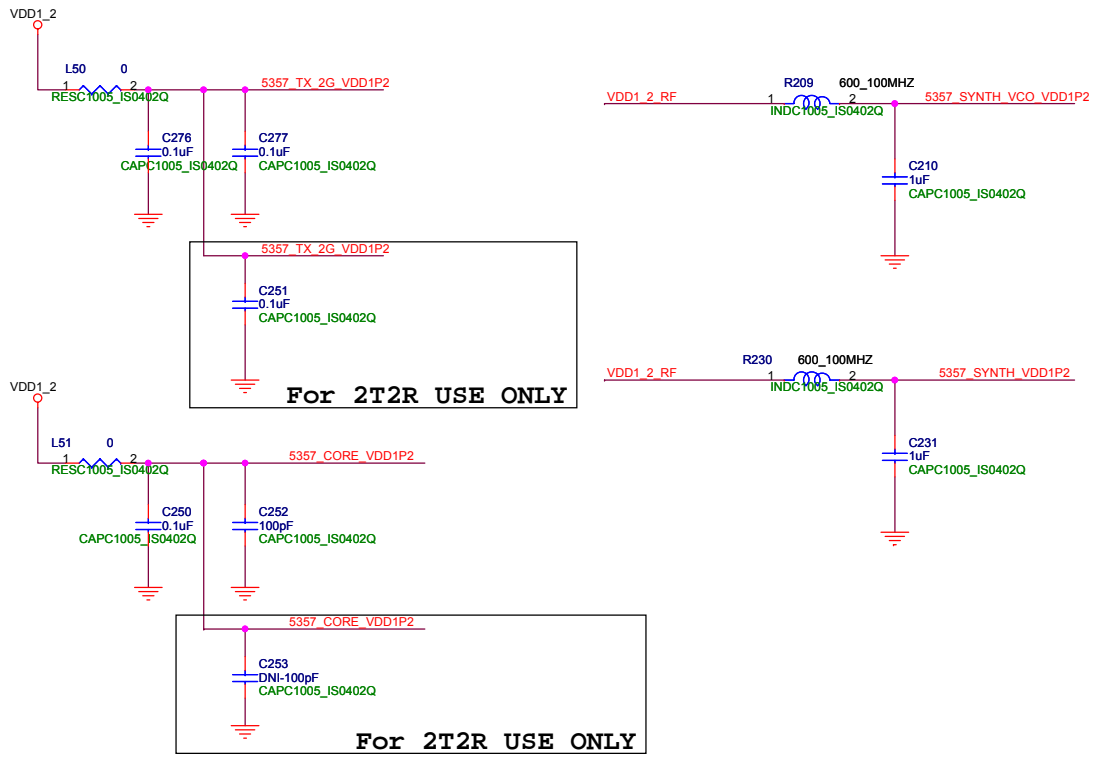
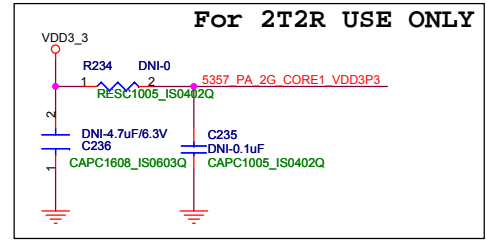
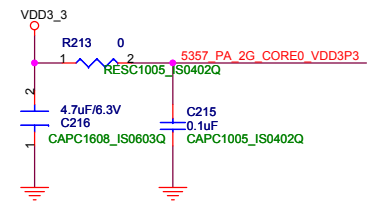
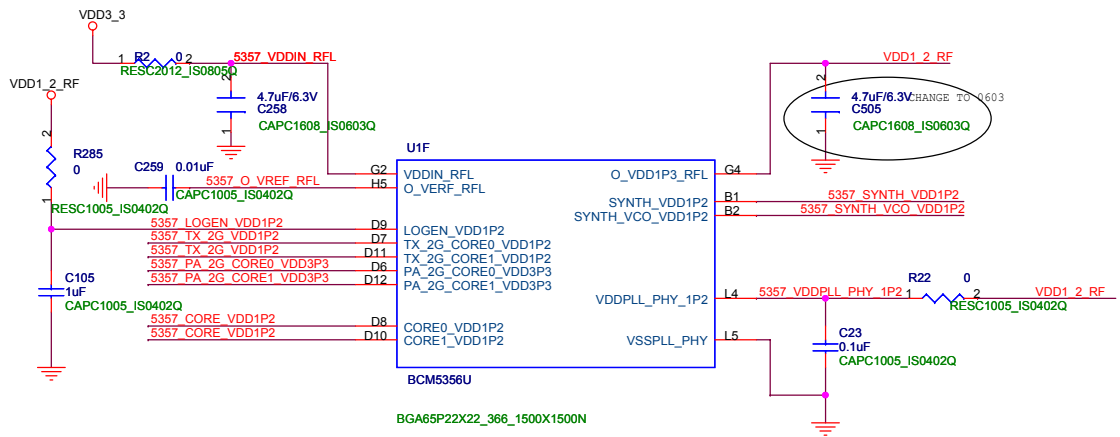
Title		
MIMO		
Size	Document Number	Rev
B	RT-N10U	A
Date:	Monday, May 23, 2011	Sheet 5 of 8



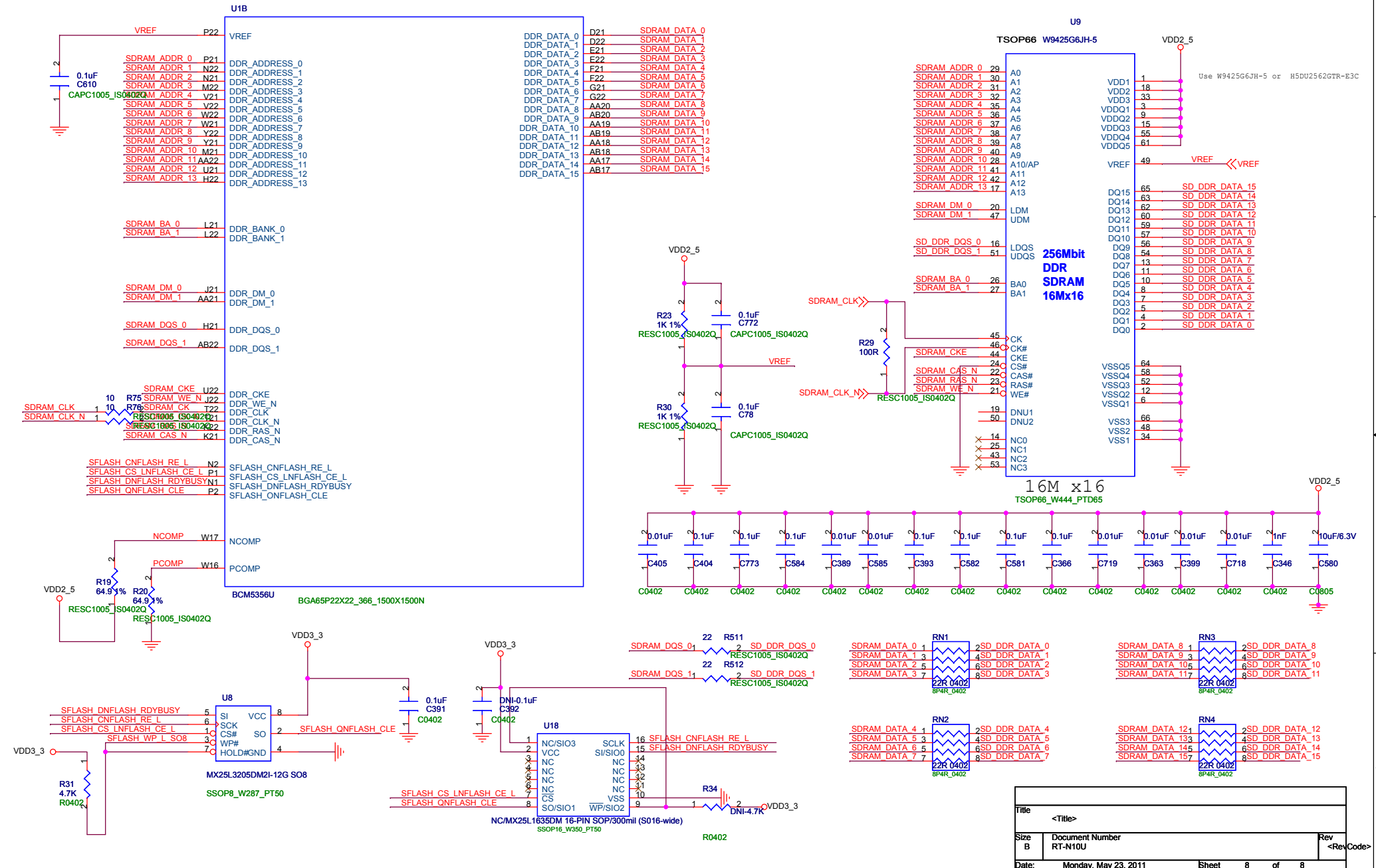
GPIO24-GPIO28 and GPIO31 were GND originally. They are changed to gpio pins for EPHY testing.



Title		
MISC		
Size	Document Number	Rev
B	RT-N10U	A
Date:	Monday, May 23, 2011	Sheet 6 of 8



Title		
POWER MODULE		
Size	Document Number	Rev
B	RT-N10U	A
Date:	Monday, May 23, 2011	Sheet 7 of 8



Title			<Title>
Size	Document Number	Rev	
B	RT-N10U	<Rev Code>	
Date:	Monday, May 23, 2011	Sheet	8 of 8