NPN Silicon Epitaxial Planar Transistor for switching and amplifier applications. Especially suitable for AF-driver stages and low power output stages.

The transistor is subdivided into three groups, G, H and I, according to its DC current gain. As complementary type the PNP transistor ST 9012 is recommended.

On special request, these transistors can be manufactured in different pin configurations.



1. Emitter 2. Base 3. Collector

TO-92 Plastic Package Weight approx. 0.19g

## Absolute Maximum Ratings (T $_a$ = 25?)

	Symbol	Value	Unit			
Collector Emitter Voltage	V <sub>CEO</sub>	30	V			
Emitter Base Voltage	$V_{EBO}$	5	V			
Collector Current	I <sub>C</sub>	800	mA			
Peak Collector Current	I <sub>CM</sub>	1	А			
Base Current	I <sub>B</sub>	100	mA			
Power Dissipation	P <sub>tot</sub>	625 <sup>1)</sup>	mW			
Junction Temperature	T <sub>j</sub>	150	ос			
Storage Temperature Range	Ts	-55 to +150	ос			
1) Valid provided that leads are kept at ambient temperature at a distance of 2 mm from case						

G S P FORM A IS AVAILABLE



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## Characteristics at T<sub>amb</sub>=25 °C

	Symbol	Min.	Тур.	Max.	Unit	
DC Current Gain						
at V <sub>CE</sub> =1V, I <sub>C</sub> =50mA						
Current Gain Group G	$h_{FE}$	110	-	183	-	
н	$h_{FE}$	177	-	250	-	
1	$h_{FE}$	250	-	380	-	
at V <sub>CE</sub> =1V, I <sub>C</sub> =500mA	$h_{FE}$	40	-	-	-	
Collector Cutoff Current						
at V <sub>CB</sub> =31V	$I_{CBO}$	-	-	100	nA	
Collector Emitter Breakdown Voltage						
at I <sub>C</sub> =1mA	$V_{(BR)CEO}$	30	-	-	V	
Emitter Base Cutoff Current						
at V <sub>EB</sub> =5.1V	$I_{EBO}$	-	-	100	nA	
Collector Saturation Voltage						
at I <sub>C</sub> =500mA, I <sub>B</sub> =20mA	$V_{CE(sat)}$	-	-	0.5	V	
Base Saturation Voltage						
at I <sub>C</sub> =500mA, I <sub>B</sub> =20mA	$V_{BE(sat)}$	-	-	1.2	V	
Base Emitter Voltage						
at V <sub>CE</sub> =1V, I <sub>C</sub> =50mA	$V_{BE}$	0.6	-	0.75	V	
Gain Bandwidth Product						
at $V_{CE}$ =5V, $I_{C}$ =10mA, f=50MHz	$f_T$	-	100	-	MHz	
Collector Base Capacitance						
at V <sub>CB</sub> =10V, f=1MHz	$C_{CBO}$		12		pF	
Thermal Resistance Junction to Ambient	R <sub>thA</sub>	-	-	200 <sup>1)</sup>	K/W	
1) Valid provided that leads are kept at ambient temperature at a distance of 2 mm from case						

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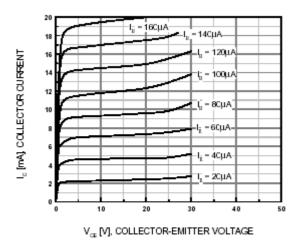


Figure 1. Static Characteristic

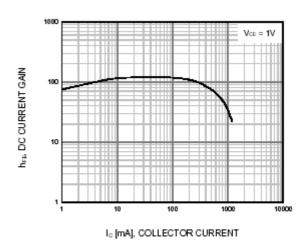


Figure 2. DC current Gain

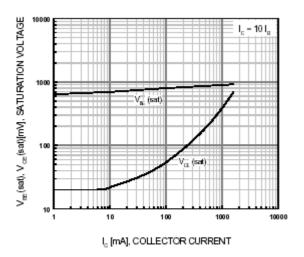


Figure 3. Base-Emitter Saturation Voltage Collector-Emitter Saturation Voltage

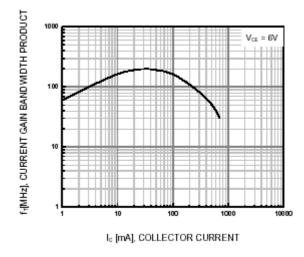


Figure 4. Current Gain Bandwidth Product

